

Docket No.: 67162-028

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :
Makoto KIDARA :
Serial No.: Group Art Unit:
Filed: October 29, 2003 Examiner:

For: CIRCUIT SIMULATION FOR A CIRCUIT INCLUDING TRANSISTORS

PRELIMINARY AMENDMENT

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination of the above-referenced application, please amend the application as follows:

IN THE BRIEF EXPLANATION OF THE DRAWINGS:

AMENDMENT TO THE BRIEF EXPLANATION OF THE DRAWINGS:

Page 4, after line 15, insert the following new paragraph:

Fig. 4 details an interpolation step performed in the process of Fig. 3.